TASCAM
TEAC Professional Division

SCHEMATIC DIAGRAM 回路図

CD-RW900SL/CD-RW901SL

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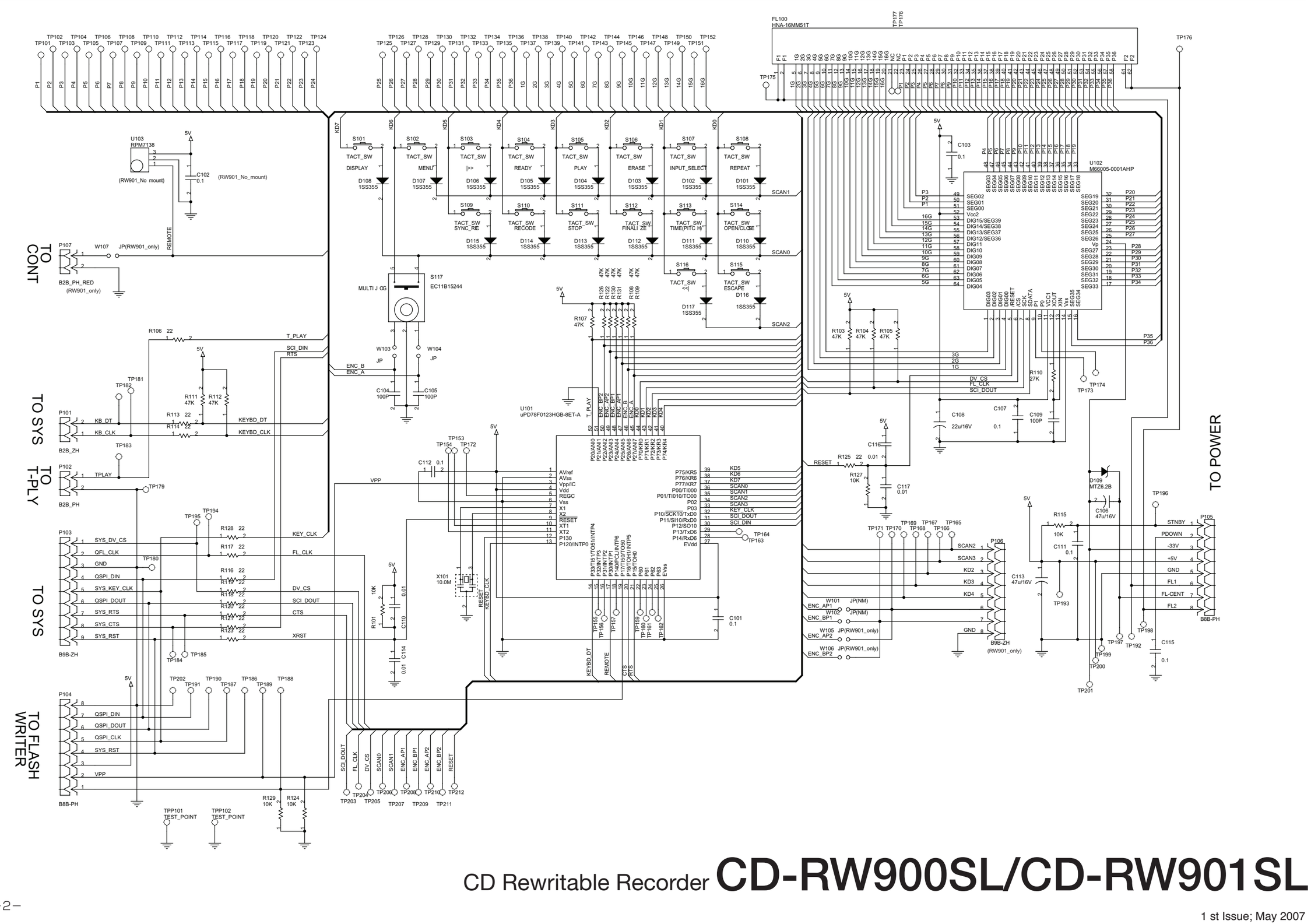
CD-RW900SL

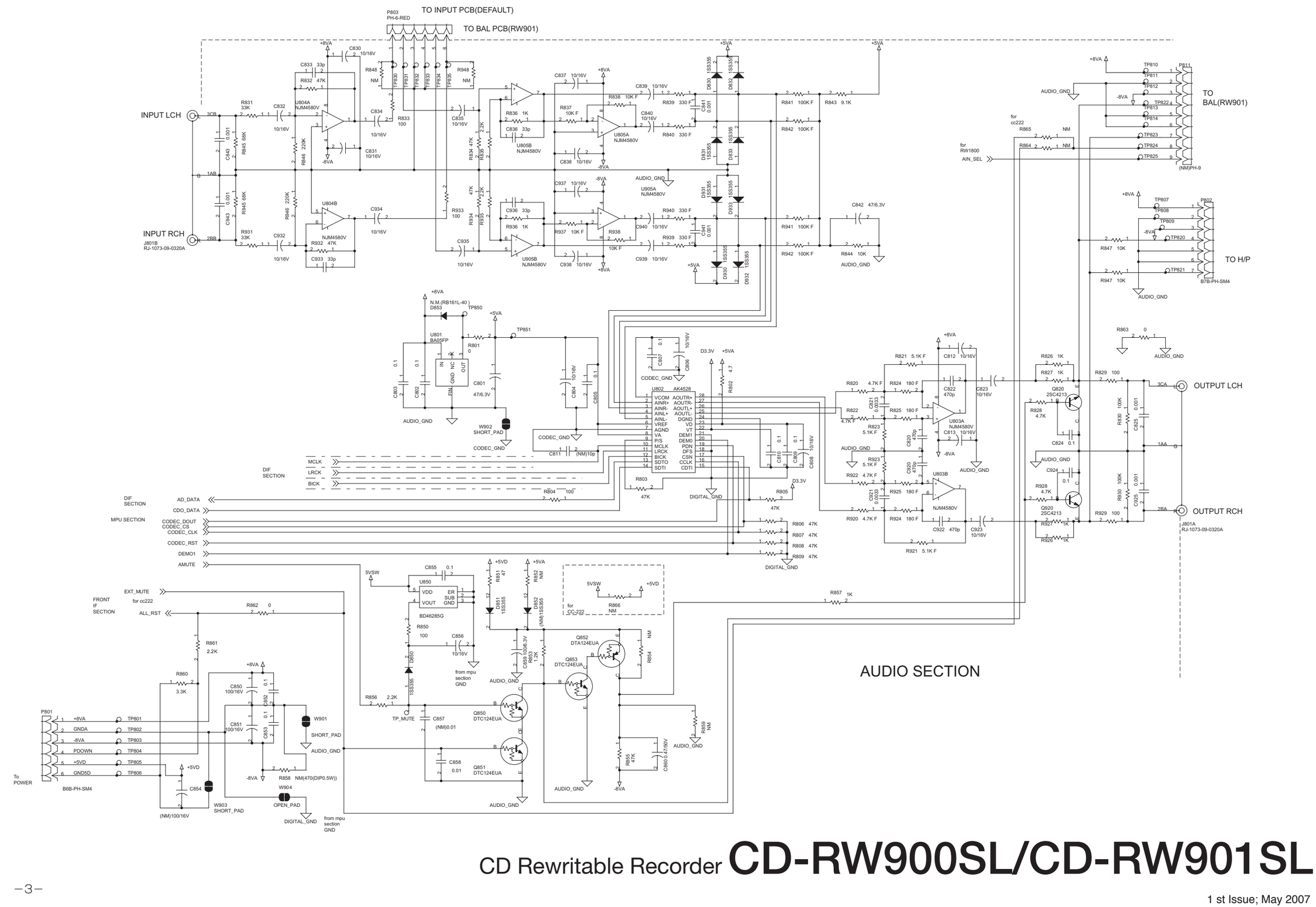
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SYS (2/3) ····· 4
SYS (3/3) ····· 5
GATHER ····· 6
REG ····· 7
SLIM ····· 8

CD-RW901SL

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SYS (1/3) ····· 10
SYS (2/3) ····· 11
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GATHER ····· 13
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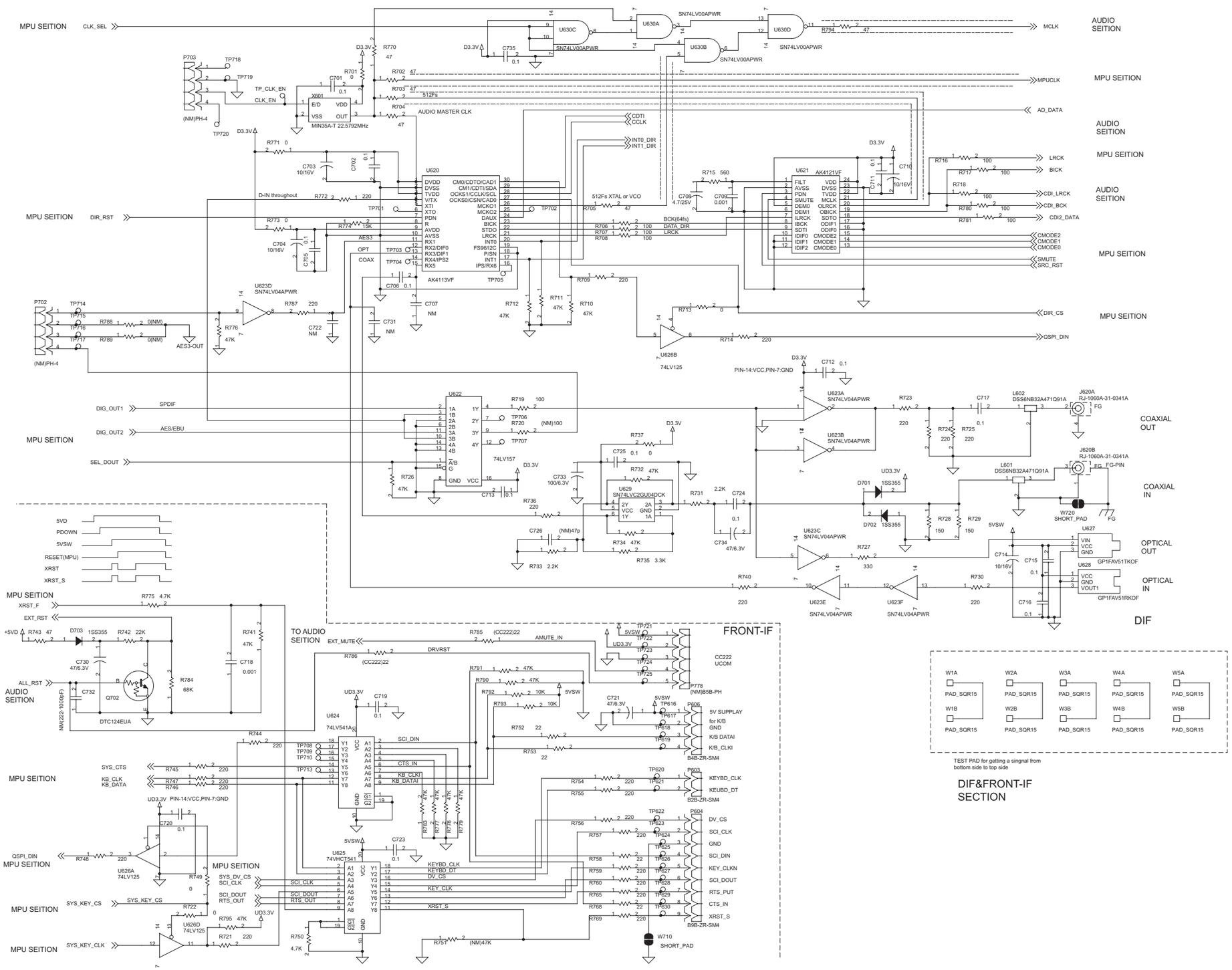
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TO CONT
TO SYS
TO T-PLY
TO SYS
TO FLASH WRITER
TO POWER
U101 uPD78F0123HGB-8ET-A
U102 M66005-0001AHP
U103 RPM7138
FL100 HNA-16MM51T
S101 TACT_SW DISPLAY
S102 TACT_SW MENU
S103 TACT_SW |>>
S104 TACT_SW READY
S105 TACT_SW PLAY
S106 TACT_SW ERASE
S107 TACT_SW INPUT_SELECT
S108 TACT_SW REPEAT
S109 TACT_SW SYNC_REC
S110 TACT_SW RECODE
S111 TACT_SW STOP
S112 TACT_SW FINALIZE
S113 TACT_SW TIME(PITCH)
S114 TACT_SW OPEN/CLOSE
S115 TACT_SW ESCAPE
S116 TACT_SW
S117 EC11B15244 MULTI JOG
SCAN0
SCAN1
SCAN2
REMOTE
W107 JP(RW901_only)
B2B_PH_RED (RW901_only)
B2B_ZH
B2B_PH
B9B-ZH
B8B-PH
B9B-ZH (RW901_only)
KB_DT
KB_CLK
KEYBD_DT
KEYBD_CLK
TPLAY
T_PLAY
SCI_DIN
RTS
KEY_CLK
FL_CLK
DV_CS
SCI_DOUT
CTS
XRST
SYS_DV_CS
QFL_CLK
GND
QSPI_DIN
QSPI_DOUT
SYS_KEY_CLK
SYS_RTS
SYS_CTS
SYS_RST
QSPI_CLK
VPP
ENC_A
ENC_B
DV_CS
FL_CLK
SCI_DOUT
SCAN0
SCAN1
ENC_AP1
ENC_BP1
ENC_AP2
ENC_BP2
RESET
X101 10.0M
TPP101 TEST_POINT
TPP102 TEST_POINT



CD Rewritable Recorder CD-RW900SL/CD-RW901SL

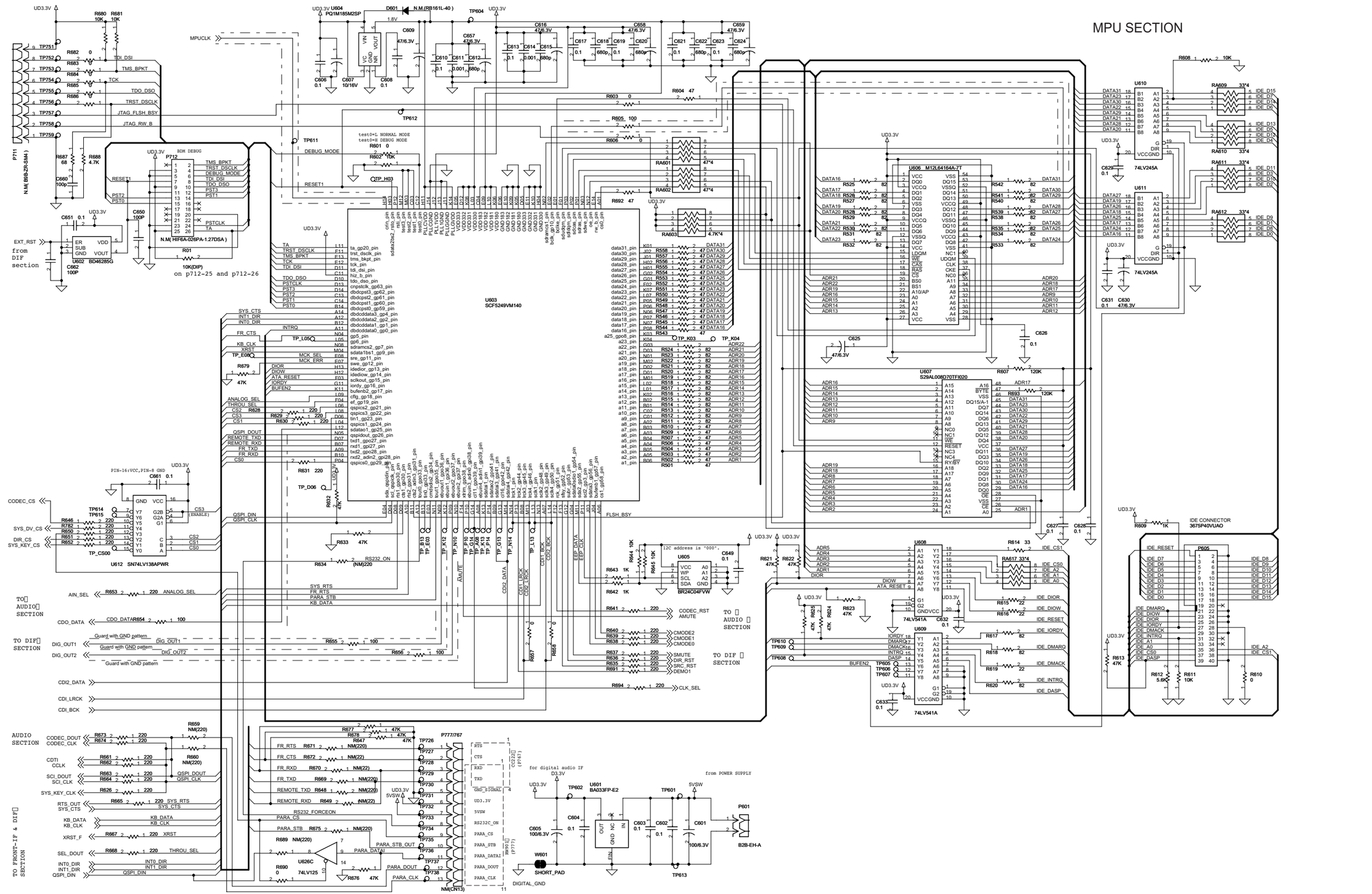
1 st Issue; May 2007

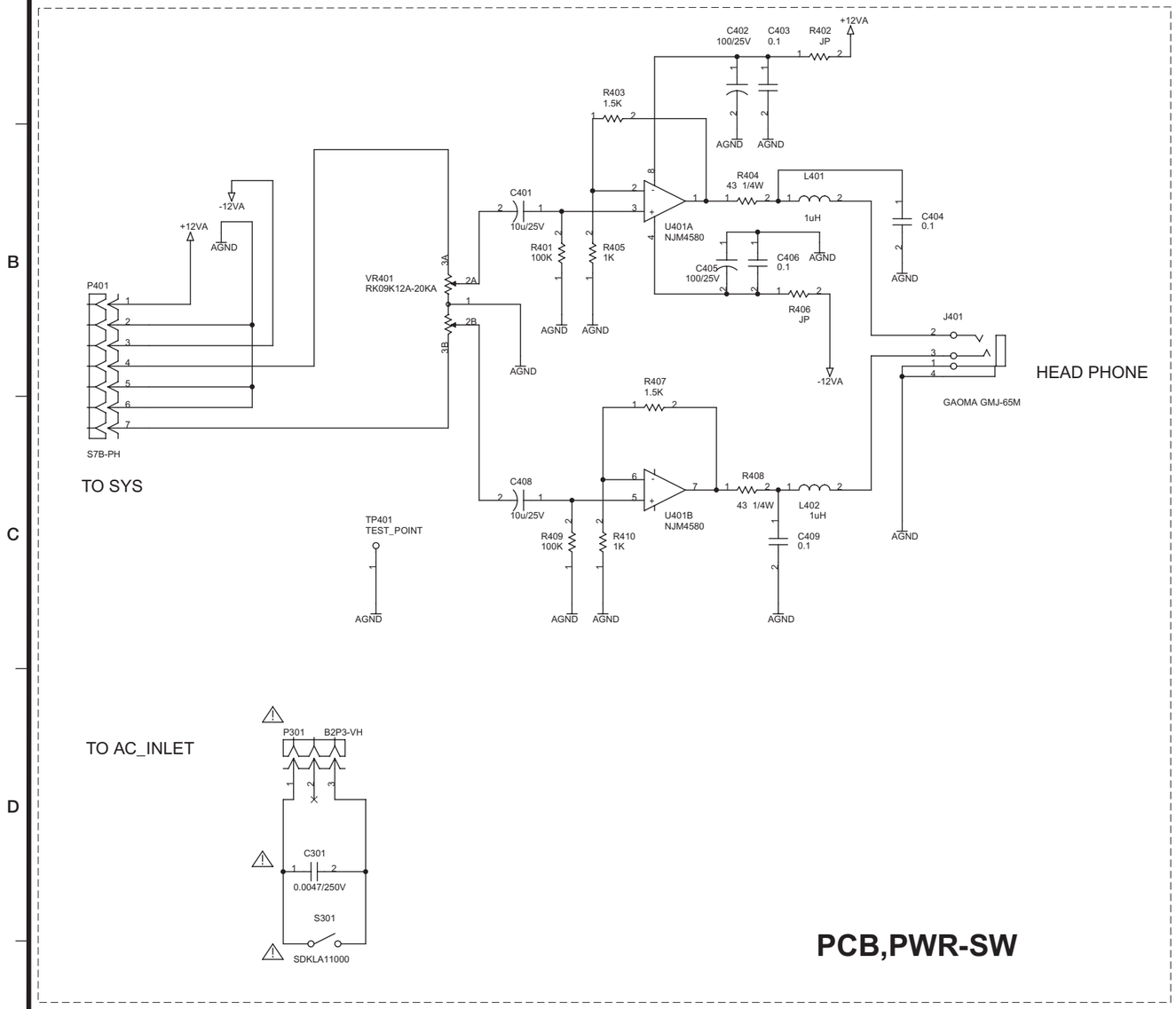


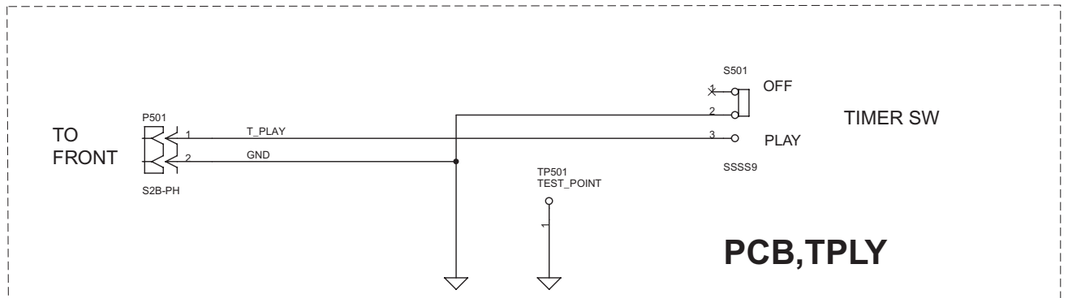
CD Rewritable Recorder **CD-RW900SL/CD-RW901SL**

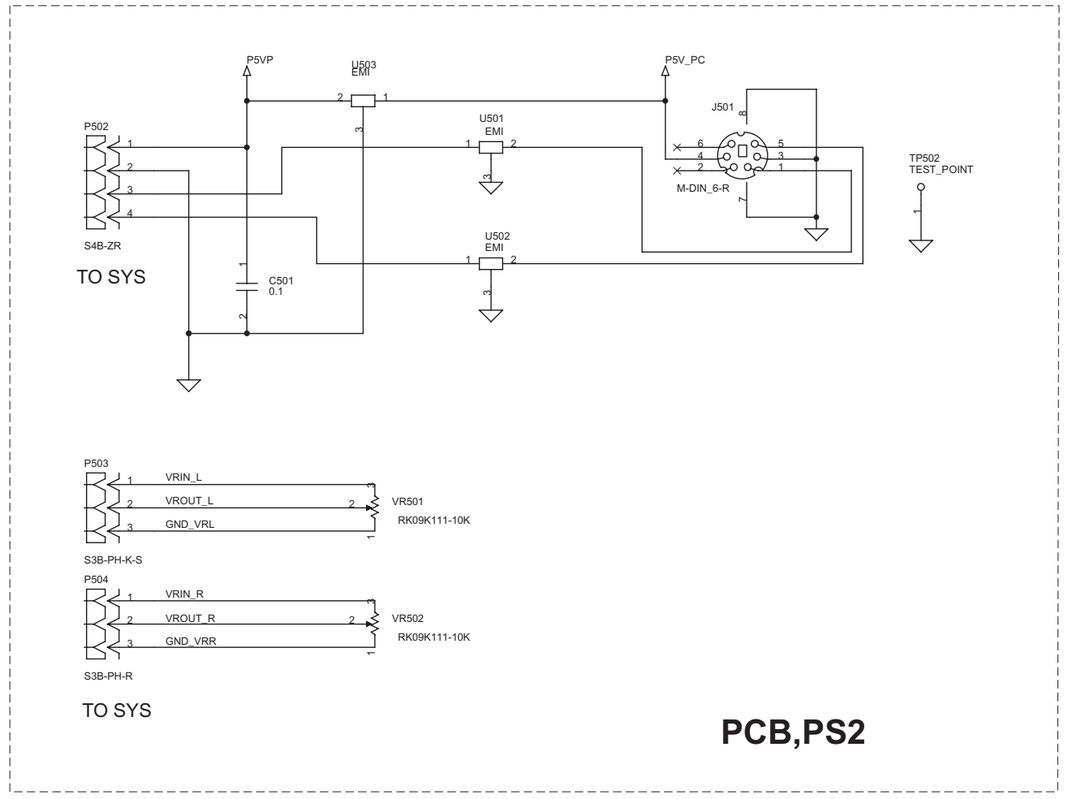
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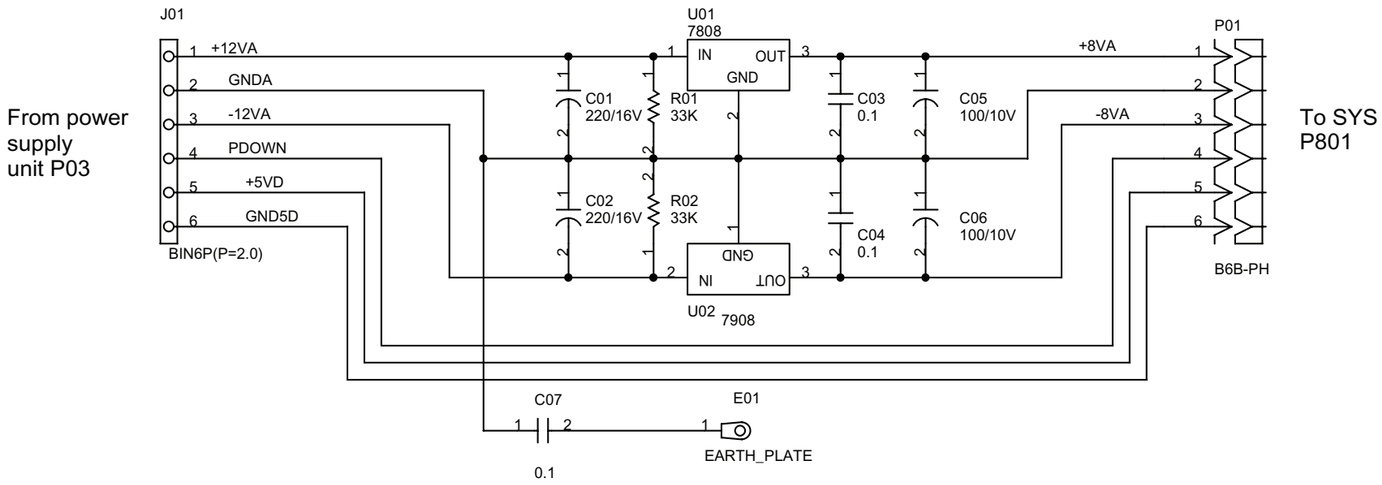
MPU SECTION

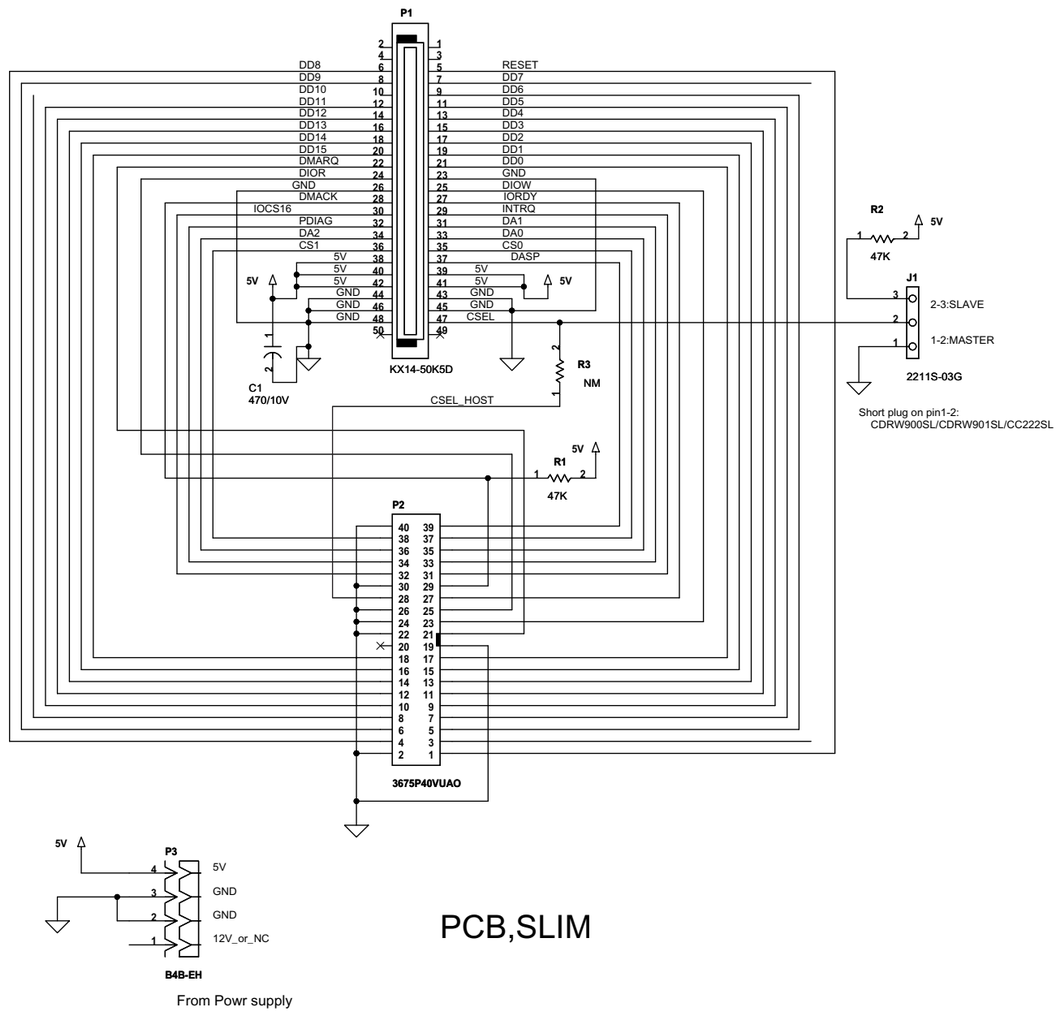

UD3.3V
MPUCLK
TDI_DSI
TMS_BPKT
TCK
TDO_DSO
TRST_DSCLK
JTAG_FLSH_BSY
JTAG_RW_B
RESET1
DEBUG_MODE
PST2
PST0
PSTCLK
TA
EXT_RST
from DIF section
BD46285G
10K(DIP)
on p712~25 and p712~26
U603
SCF5249VM140
U606 M12L64164A-7T
U607
S29AL008D70TFI020
U608
U609
U610
U611
U612 SN74LV138APWR
74LV245A
74LV541A
U626C
74LV125
U604 PQ1M185M2SP
U605
BR24C04FVW
U601 BA033FP-E2
IDE CONNECTOR
3675P40VUAO
AUDIO SECTION
TO AUDIO SECTION
TO DIF SECTION
TO FRONT-IF & DIF SECTION
DIGITAL_GND
SHORT_PAD
B2B-EH-A


C402 100/25V
C403 0.1
R402 JP
+12VA
R403 1.5K
AGND
R404 43 1/4W
L401 1uH
C401 10u/25V
U401A NJM4580
C404 0.1
R401 100K
R405 1K
C405 100/25V
C406 0.1
R406 JP
-12VA
J401
HEAD PHONE
GAOMA GMJ-65M
P401
S7B-PH
TO SYS
VR401 RK09K12A-20KA
R407 1.5K
C408 10u/25V
U401B NJM4580
R408 43 1/4W
L402 1uH
C409 0.1
R409 100K
R410 1K
TP401 TEST_POINT
TO AC_INLET
P301 B2P3-VH
C301 0.0047/250V
S301
SDKLA11000
PCB,PWR-SW

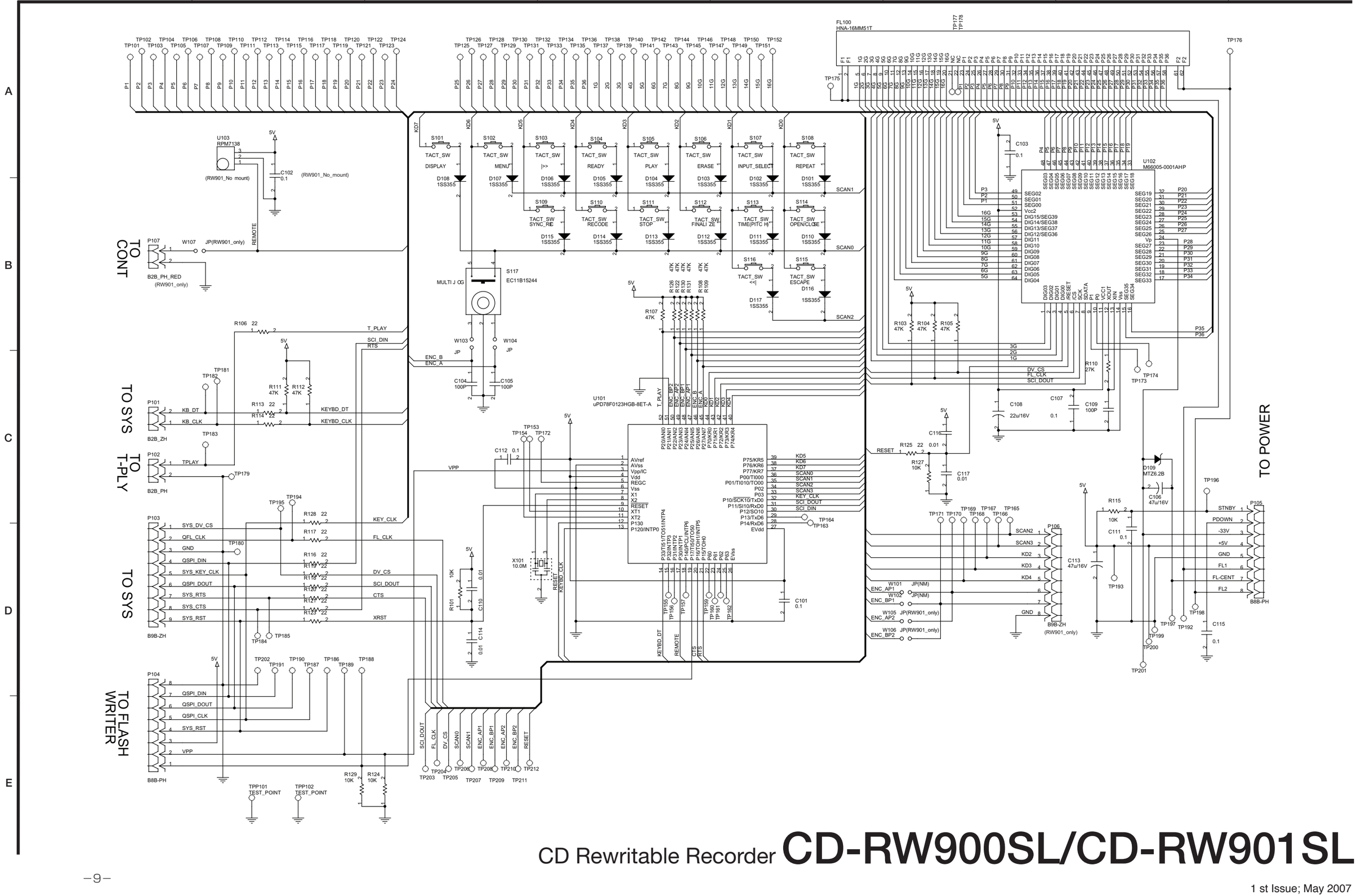

TO FRONT
P501
S2B-PH
T_PLAY
GND
S501
OFF
PLAY
SSSS9
TIMER SW
TP501 TEST_POINT
PCB,TPLY

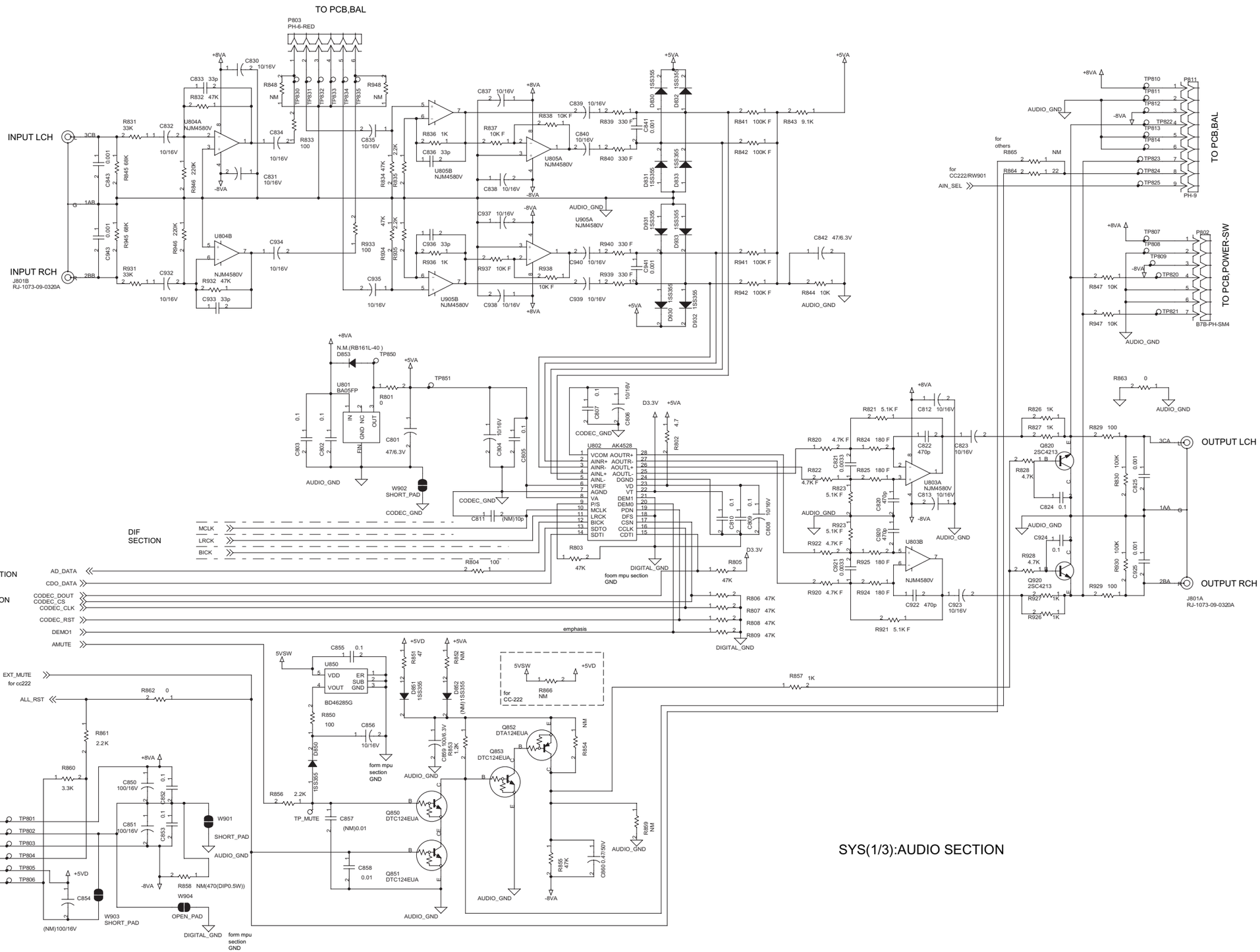

P5VP
U503 EMI
P5V_PC
J501
M-DIN_6-R
TP502 TEST_POINT
P502
S4B-ZR
TO SYS
U501 EMI
U502 EMI
C501 0.1
P503
VRIN_L
VROUT_L
GND_VRL
VR501 RK09K111-10K
S3B-PH-K-S
P504
VRIN_R
VROUT_R
GND_VRR
VR502 RK09K111-10K
S3B-PH-R
TO SYS
PCB,PS2


From power supply unit P03
J01
1 +12VA
2 GNDA
3 -12VA
4 PDOWN
5 +5VD
6 GND5D
BIN6P(P=2.0)
C01 220/16V
C02 220/16V
R01 33K
R02 33K
U01 7808
IN
OUT
GND
U02 7908
C03 0.1
C04 0.1
C05 100/10V
C06 100/10V
+8VA
-8VA
P01
B6B-PH
To SYS P801
C07
0.1
E01
EARTH_PLATE

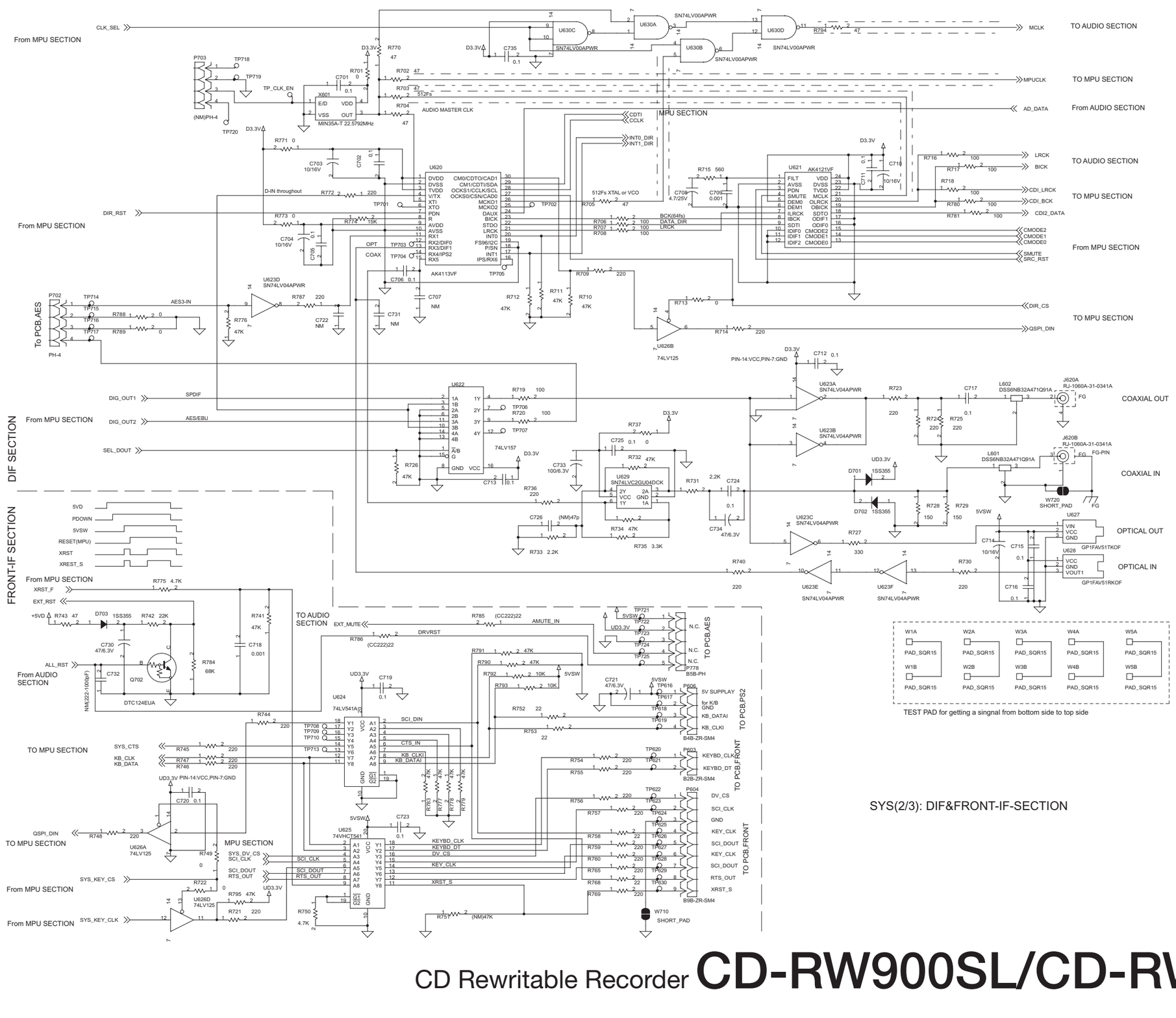

P1
DD8
DD9
DD10
DD11
DD12
DD13
DD14
DD15
DMARQ
DIOR
GND
DMACK
IOCS16
PDIAG
DA2
CS1
5V
5V
5V
GND
GND
GND
RESET
DD7
DD6
DD5
DD4
DD3
DD2
DD1
DD0
GND
DIOW
IORDY
INTRQ
DA1
DA0
CS0
DASP
5V
5V
GND
GND
CSEL
KX14-50K5D
C1
470/10V
R3
NM
CSEL_HOST
R1
47K
R2
47K
J1
2-3:SLAVE
1-2:MASTER
2211S-03G
Short plug on pin1-2:
CDRW900SL/CDRW901SL/CC222SL
P2
3675P40VUAO
P3
5V
GND
GND
12V_or_NC
B4B-EH
From Powr supply

PCB,SLIM


TO CONT
TO SYS
TO T-PLY
TO SYS
TO FLASH WRITER
TO POWER
U101 uPD78F0123HGB-8ET-A
U102 M66005-0001AHP
U103 RPM7138
FL100 HNA-16MM51T
S101 TACT_SW DISPLAY
S102 TACT_SW MENU
S103 TACT_SW |>>
S104 TACT_SW READY
S105 TACT_SW PLAY
S106 TACT_SW ERASE
S107 TACT_SW INPUT_SELECT
S108 TACT_SW REPEAT
S109 TACT_SW SYNC_RE
S110 TACT_SW RECODE
S111 TACT_SW STOP
S112 TACT_SW FINALI ZE
S113 TACT_SW TIME(PITC H)
S114 TACT_SW OPEN/CLOSE
S115 TACT_SW ESCAPE
S116 TACT_SW
S117 EC11B15244 MULTI J OG
SCAN0
SCAN1
SCAN2
REMOTE
JP(RW901_only)
JP(RW901_No_mount)
B2B_PH_RED (RW901_only)
B2B_ZH
B2B_PH
B9B-ZH
B8B-PH
B9B-ZH (RW901_only)
DV_CS
FL_CLK
SCI_DOUT
KEY_CLK
KEYBD_DT
KEYBD_CLK
SCI_DIN
RESET
VPP
T_PLAY
SCI_DIN
RTS
CTS
XRST
TPLAY
QSPI_DIN
QSPI_DOUT
QSPI_CLK
SYS_RST
SYS_DV_CS
QFL_CLK
GND
QSPI_DIN
SYS_KEY_CLK
QSPI_DOUT
SYS_RTS
SYS_CTS
SYS_RST
TEST_POINT
5V

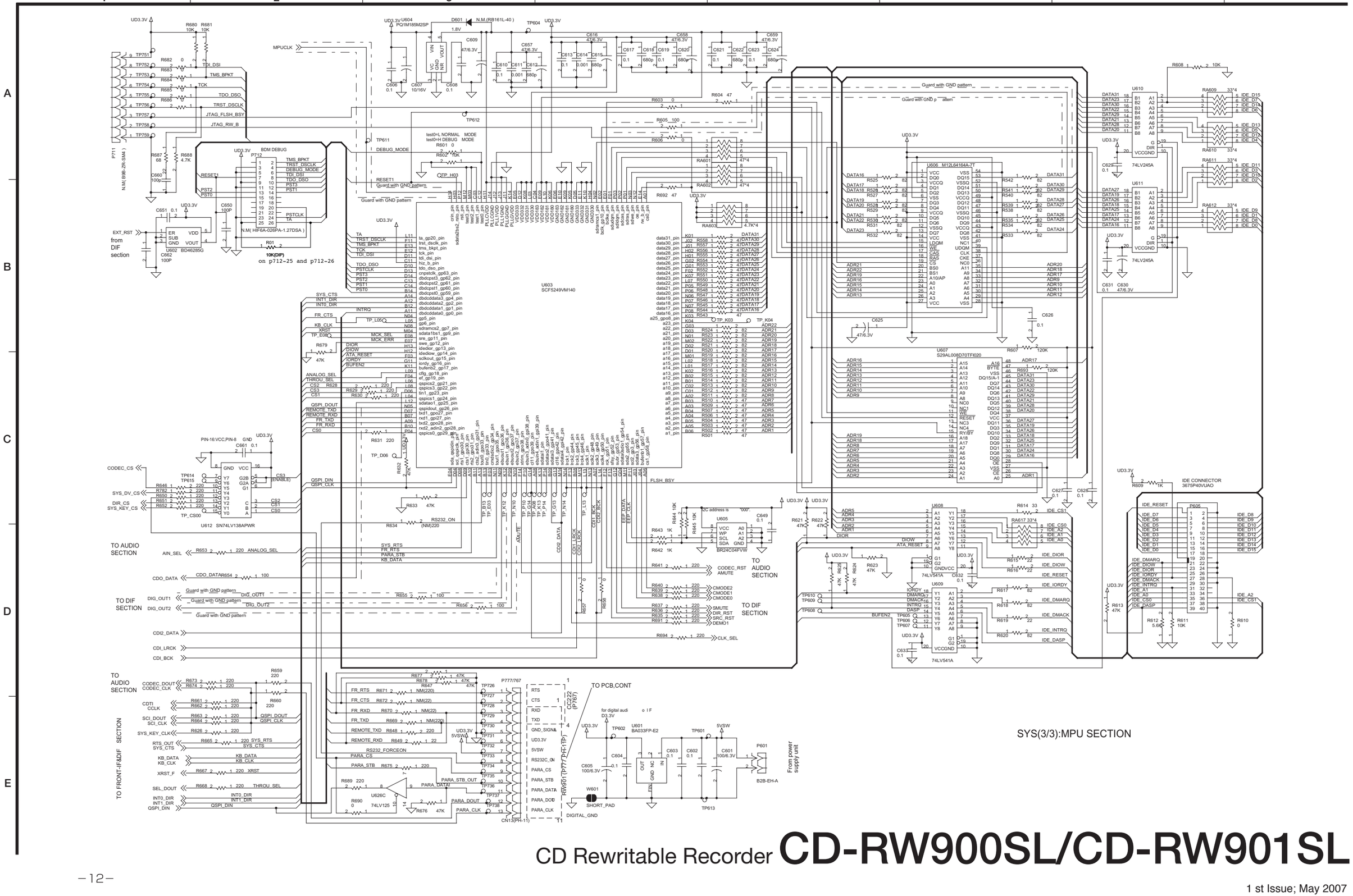


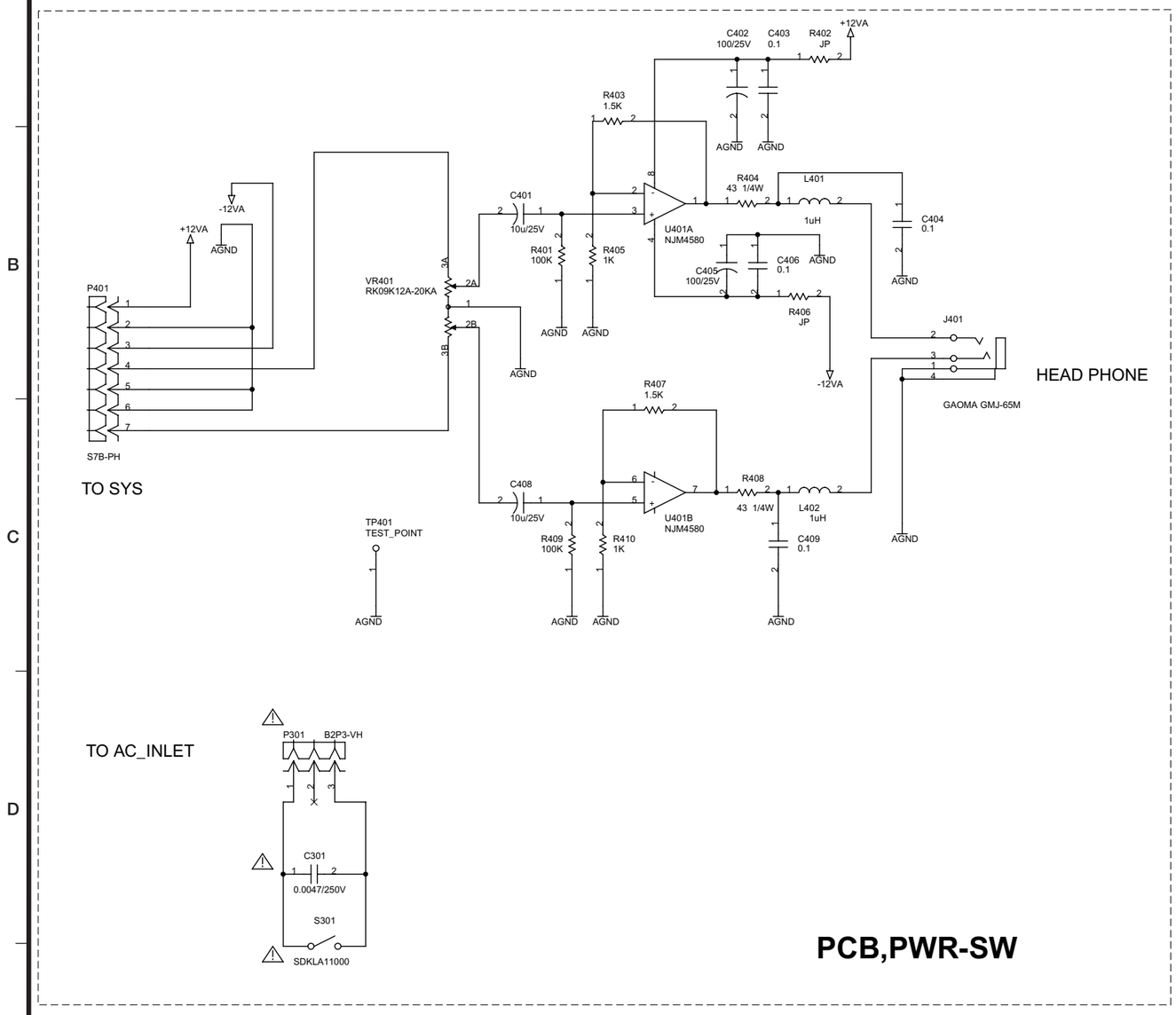
CD Rewritable Recorder **CD-RW900SL/CD-RW901SL**

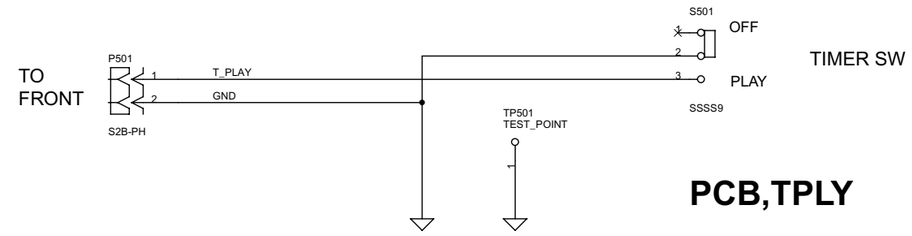


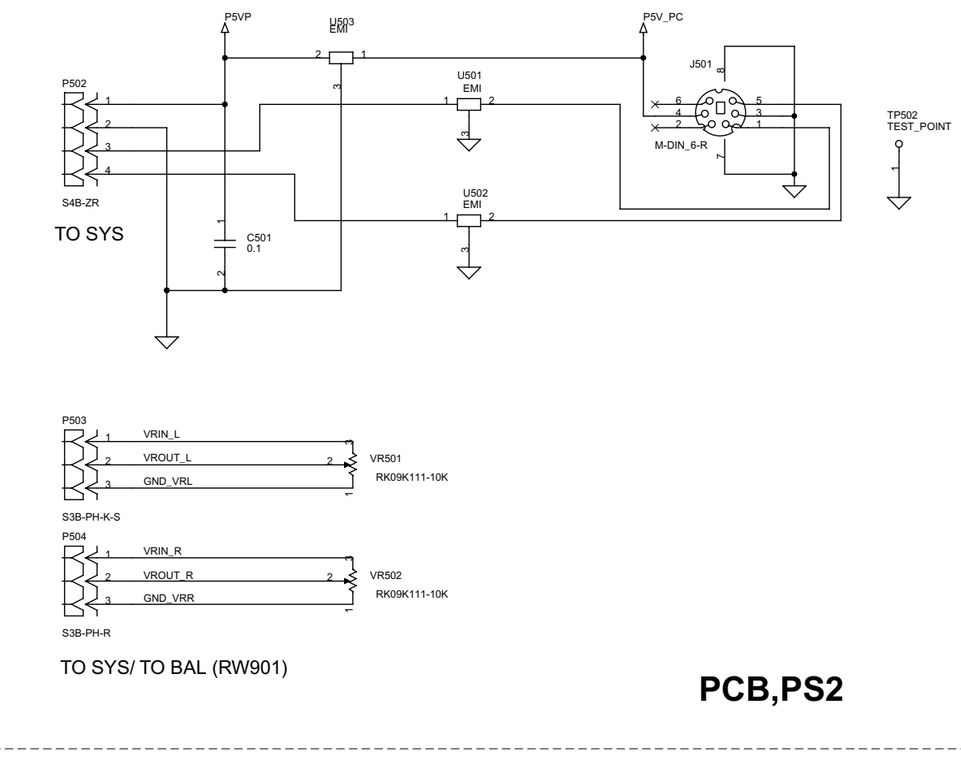
SYS(2/3): DIF&FRONT-IF-SECTION

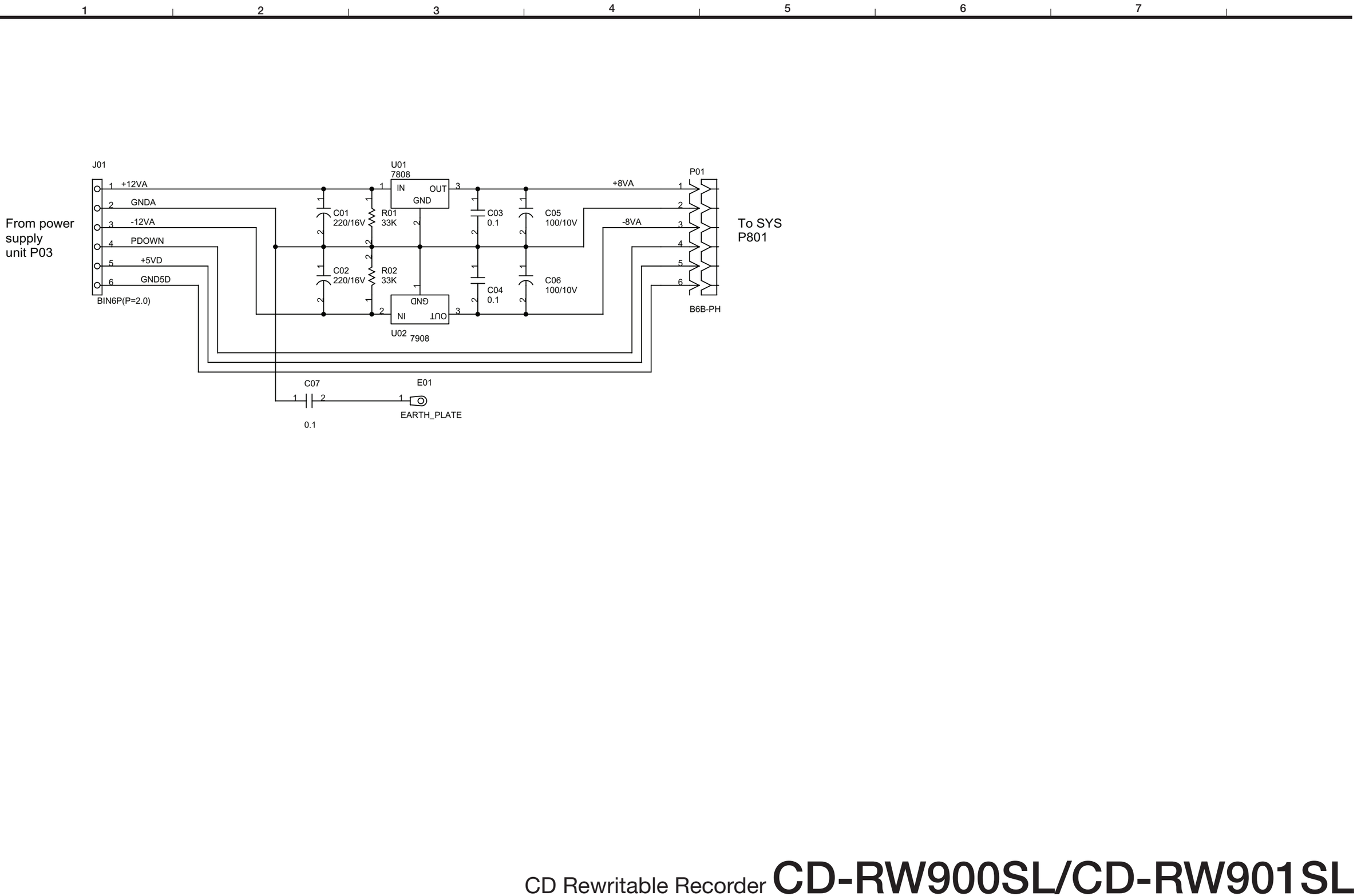
CD Rewritable Recorder **CD-RW900SL/CD-RW901SL**

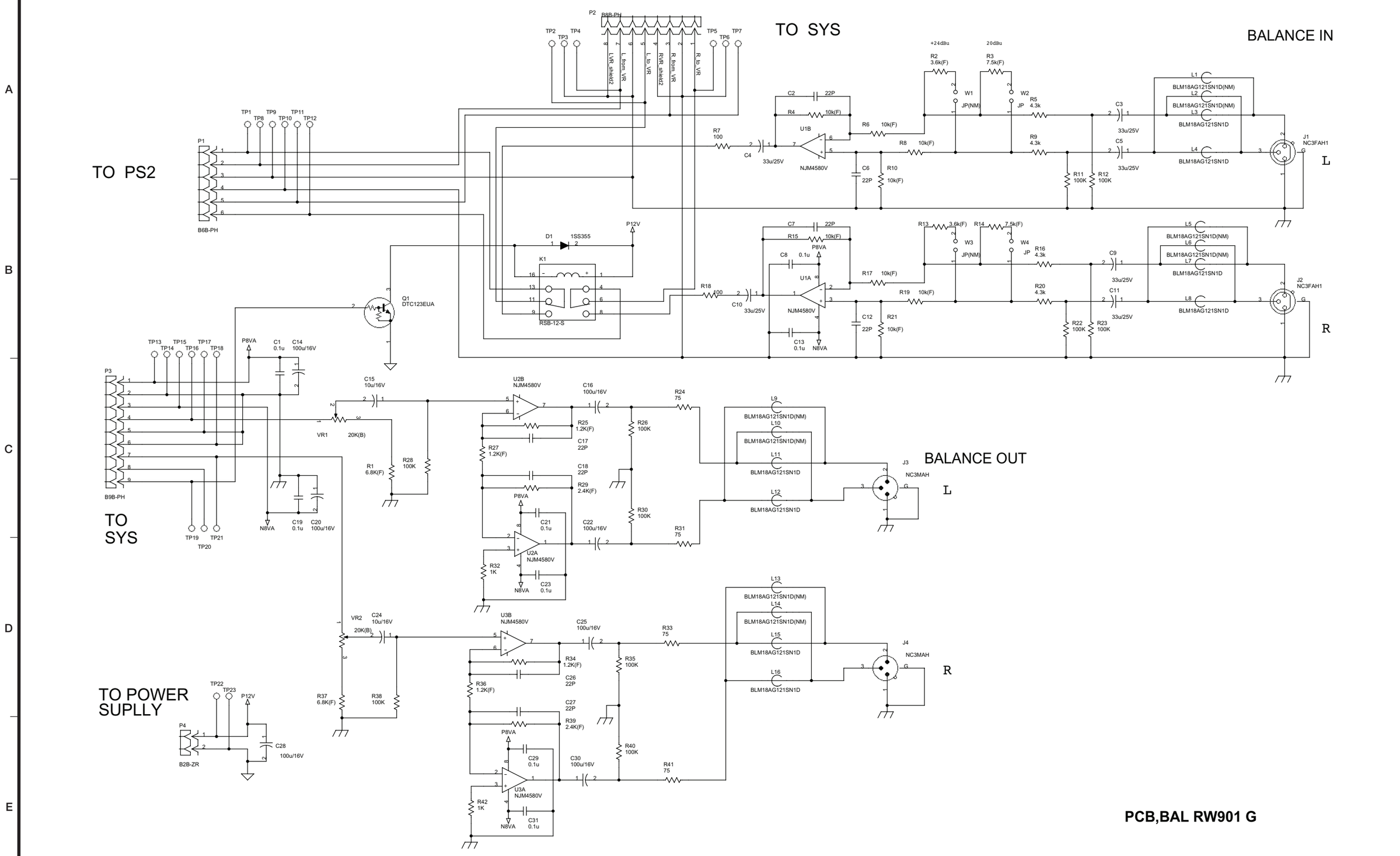

1
2
3
4
5
6
7
A
B
C
D
E
U603
SCF5249VM140
U606 M12L64164A-7T
U607
S29AL008D70TFI020
U608
U609
U610
U611
U612 SN74LV138APWR
U605
BR24C04FVW
U602 BD46285G
U604 PQ1M185M2SP
D601 N.M(RB161L-40)
TP604
TP612
TP611
DEBUG_MODE
test0=L NORMAL MODE
test0=H DEBUG MODE
TP_H03
RESET1
Guard with GND pattern
BDM DEBUG
P712
P711
N.M(B9B-ZR-SM4)
N.M(HIF6A-026PA-1.27DSA)
10K(DIP)
on p712~25 and p712~26
EXT_RST
from DIF section
MPUCLK
1.8V
UD3.3V
TMS_BPKT
TRST_DSCLK
DEBUG_MODE
TDI_DSI
TDO_DSO
PST3
PST1
PSTCLK
TA
TDI_DSI
TMS_BPKT
TCK
TDO_DSO
JTAG_FLSH_BSY
JTAG_RW_B
TRST_DSCLK
FLSH_BSY
SYS_CTS
INT1_DIR
INT0_DIR
FR_CTS
KB_CLK
XRST
INTRQ
MCK_SEL
MCK_ERR
DIOR
DIOW
ATA_RESET
IORDY
DASP
BUFEN2
ANALOG_SEL
THROU_SEL
CS2
CS3
CS1
QSPI_DOUT
REMOTE_TXD
REMOTE_RXD
FR_TXD
FR_RXD
CS0
QSPI_DIN
QSPI_CLK
CODEC_CS
SYS_DV_CS
DIR_CS
SYS_KEY_CS
AIN_SEL
ANALOG_SEL
CDO_DATA
DIG_OUT1
DIG_OUT2
CDI2_DATA
CDI_LRCK
CDI_BCK
SYS_RTS
FR_RTS
PARA_STB
KB_DATA
CODEC_RST
AMUTE
CMODE2
CMODE1
CMODE0
SMUTE
DIR_RST
SRC_RST
DEMO1
CLK_SEL
EEP_DATA
EEP_CLK
FR_RTS
FR_CTS
FR_RXD
FR_TXD
REMOTE_TXD
REMOTE_RXD
RS232_FORCEON
PARA_CS
PARA_STB
PARA_DATA
PARA_DOUT
PARA_CLK
PARA_STB_OUT
CODEC_DOUT
CODEC_CLK
CDTI
CCLK
SCI_DOUT
SCI_CLK
SYS_KEY_CLK
RTS_OUT
SYS_CTS
KB_DATA
KB_CLK
XRST_F
SEL_DOUT
INT0_DIR
INT1_DIR
QSPI_DIN
QSPI_DOUT
QSPI_CLK
SYS_RTS
SYS_CTS
THROU_SEL
TO AUDIO SECTION
TO DIF SECTION
TO FRONT-IF&DIF SECTION
TO PCB,CONT
P777/767
CN13(PH-11)
RW901(P777-PH-1TP)
CC222 (P767)
DIGITAL_GND
SHORT_PAD
B2B-EH-A
From power supply unit
for digital audi o I F
D3.3V
5VSW
IDE CONNECTOR 3675P40VUAO
74LV245A
74LV541A
BDM DEBUG
Guard with GND pattern
SYS(3/3):MPU SECTION


+12VA
C402 100/25V
C403 0.1
R402 JP
R403 1.5K
AGND
R404 43 1/4W
L401 1uH
C401 10u/25V
U401A NJM4580
R401 100K
R405 1K
C404 0.1
C405 100/25V
C406 0.1
R406 JP
-12VA
J401
VR401 RK09K12A-20KA
P401
S7B-PH
TO SYS
HEAD PHONE
GAOMA GMJ-65M
R407 1.5K
C408 10u/25V
U401B NJM4580
R408 43 1/4W
L402 1uH
C409 0.1
R409 100K
R410 1K
TP401 TEST_POINT
TO AC_INLET
P301 B2P3-VH
C301 0.0047/250V
S301
SDKLA11000
PCB,PWR-SW


TO FRONT
P501
T_PLAY
GND
S2B-PH
S501
OFF
PLAY
SSSS9
TIMER SW
TP501 TEST_POINT
PCB,TPLY


P5VP
U503 EMI
P5V_PC
J501
P502
S4B-ZR
TO SYS
U501 EMI
U502 EMI
C501 0.1
M-DIN_6-R
TP502 TEST_POINT
P503
VRIN_L
VROUT_L
GND_VRL
S3B-PH-K-S
VR501 RK09K111-10K
P504
VRIN_R
VROUT_R
GND_VRR
S3B-PH-R
VR502 RK09K111-10K
TO SYS/ TO BAL (RW901)
PCB,PS2

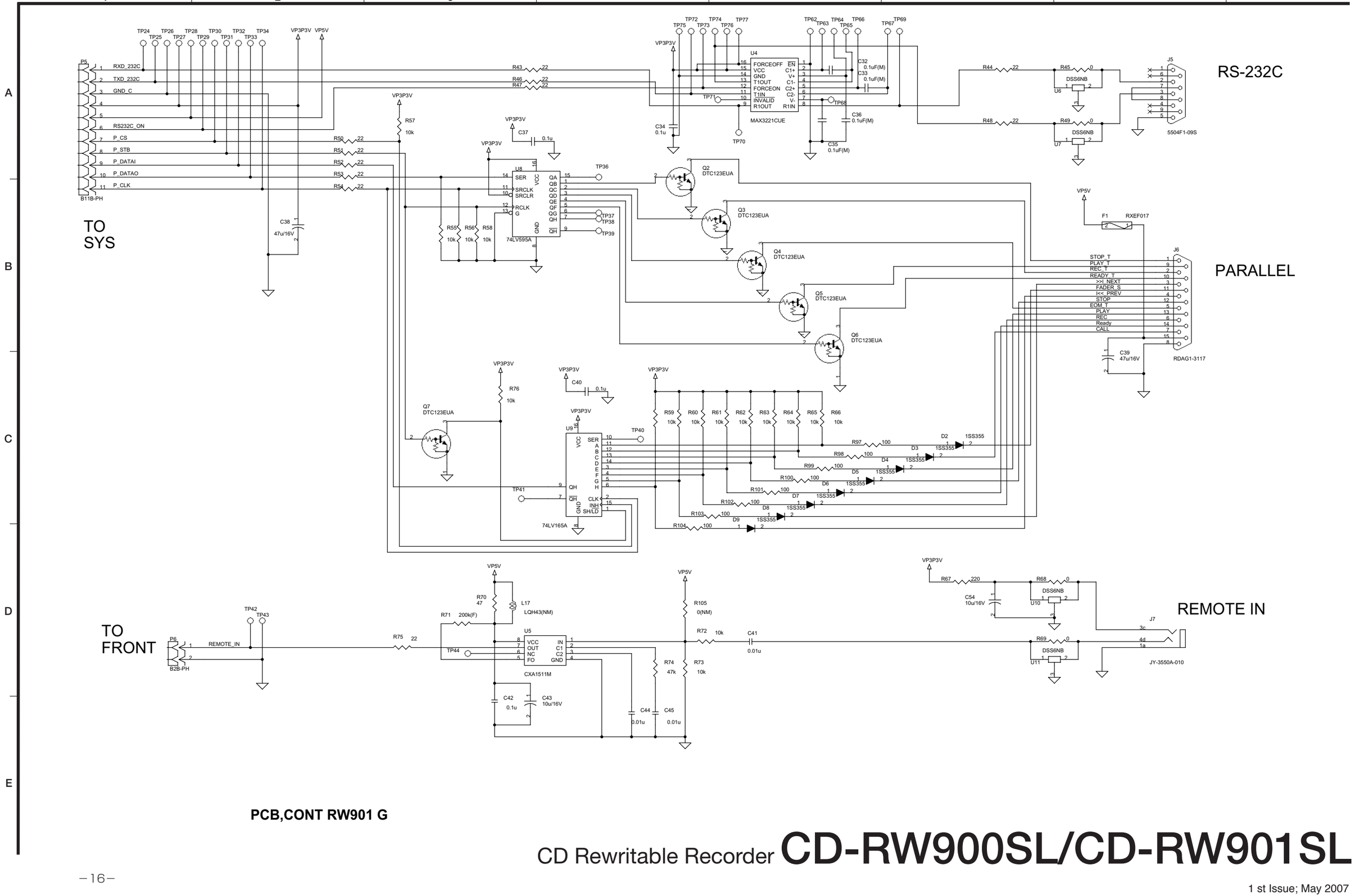

1
2
3
4
5
6
7
A
B
C
D
E
From power supply unit P03
J01
1 +12VA
2 GNDA
3 -12VA
4 PDOWN
5 +5VD
6 GND5D
BIN6P(P=2.0)
U01
7808
IN
OUT
GND
U02
7908
GND
IN
OUT
C01 220/16V
C02 220/16V
C03 0.1
C04 0.1
C05 100/10V
C06 100/10V
R01 33K
R02 33K
C07
0.1
E01
EARTH_PLATE
+8VA
-8VA
P01
B6B-PH
To SYS P801



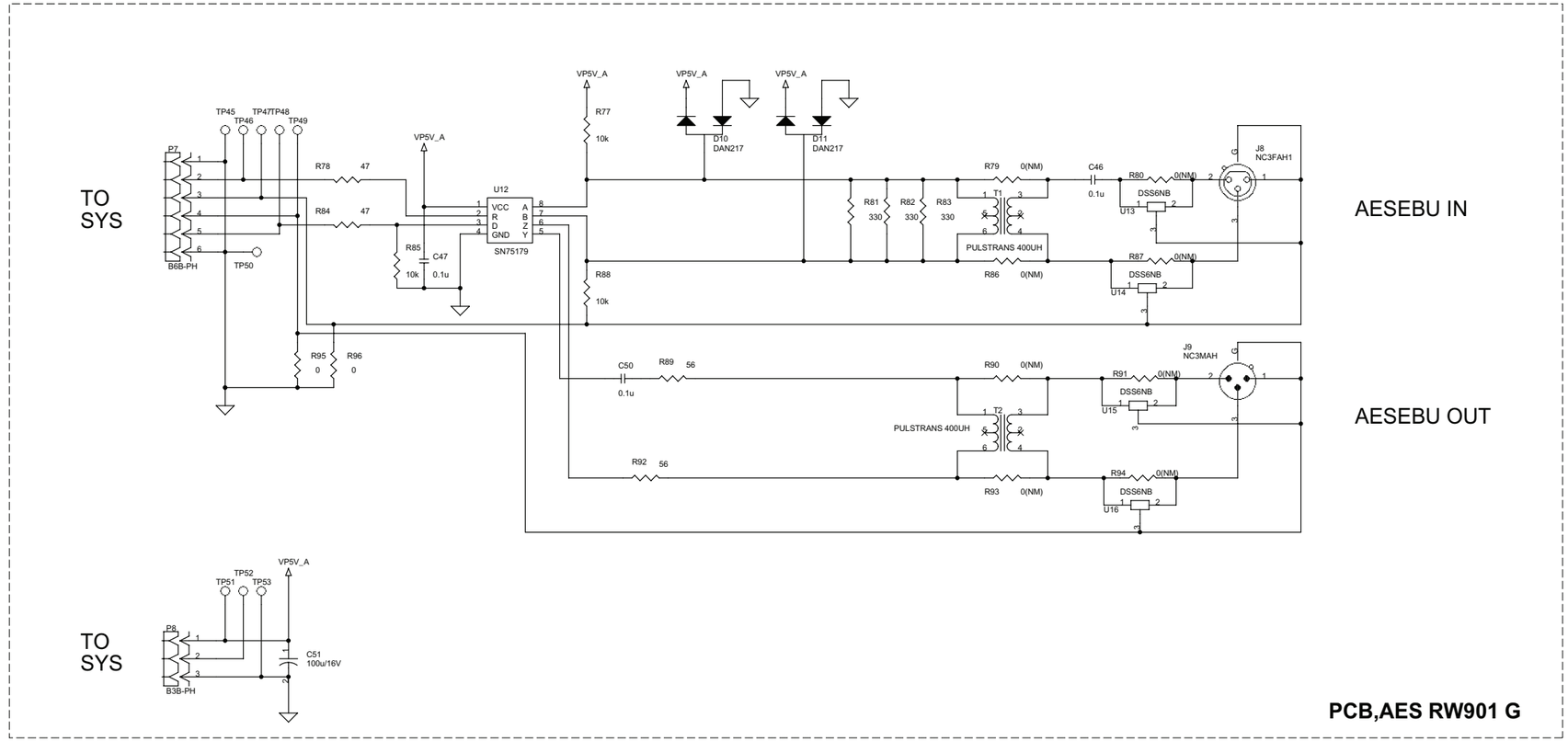
PCB,BAL RW901 G

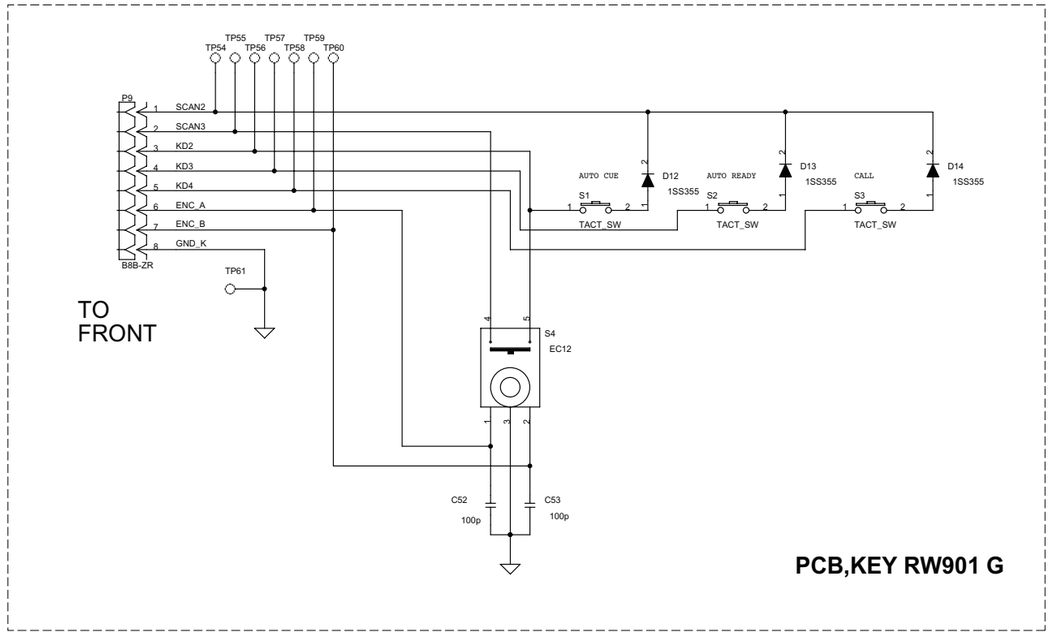
CD Rewritable Recorder **CD-RW900SL/CD-RW901SL**

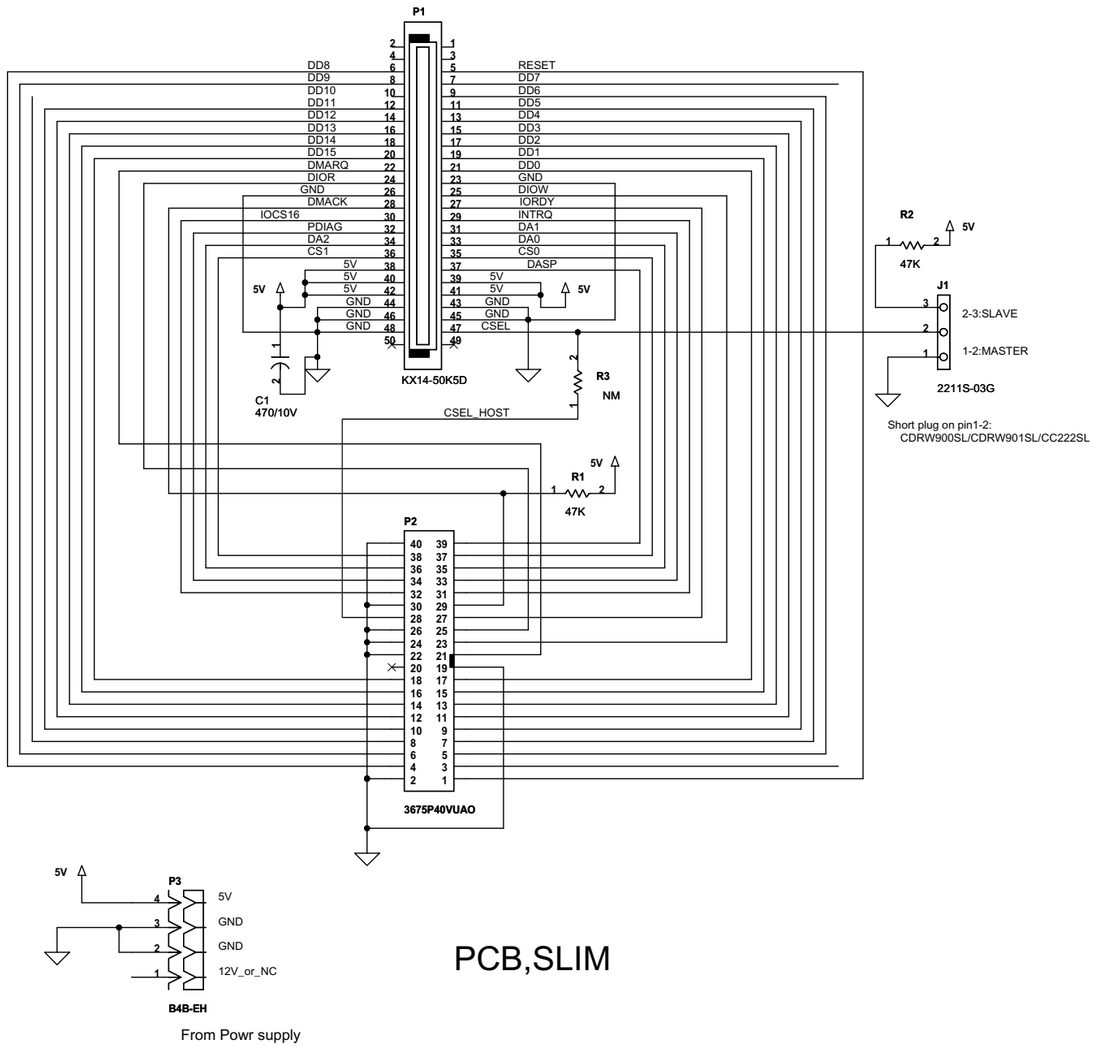
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PCB,CONT RW901 G


TO SYS
TO SYS
AESEBU IN
AESEBU OUT
PCB,AES RW901 G
P7
B6B-PH
TP45
TP46
TP47TP48
TP49
TP50
R78 47
R84 47
R85 10k
C47 0.1u
U12
SN75179
VCC
R
D
GND
A
B
Z
Y
VP5V_A
R77 10k
R88 10k
D10 DAN217
D11 DAN217
R81 330
R82 330
R83 330
R79 0(NM)
T1
PULSTRANS 400UH
R86 0(NM)
C46 0.1u
R80 0(NM)
DSS6NB
U13
R87 0(NM)
U14
J8 NC3FAH1
R95 0
R96 0
C50 0.1u
R89 56
R92 56
R90 0(NM)
T2
PULSTRANS 400UH
R93 0(NM)
R91 0(NM)
U15
R94 0(NM)
U16
J9 NC3MAH
P8
B3B-PH
TP51
TP52
TP53
C51 100u/16V


TO FRONT
P9
B8B-ZR
TP54
TP55
TP56
TP57
TP58
TP59
TP60
TP61
SCAN2
SCAN3
KD2
KD3
KD4
ENC_A
ENC_B
GND_K
AUTO CUE
AUTO READY
CALL
D12 1SS355
D13 1SS355
D14 1SS355
S1 TACT_SW
S2 TACT_SW
S3 TACT_SW
S4 EC12
C52 100p
C53 100p
PCB,KEY RW901 G


P1
DD8
DD9
DD10
DD11
DD12
DD13
DD14
DD15
DMARQ
DIOR
GND
DMACK
IOCS16
PDIAG
DA2
CS1
5V
5V
5V
GND
GND
GND
RESET
DD7
DD6
DD5
DD4
DD3
DD2
DD1
DD0
GND
DIOW
IORDY
INTRQ
DA1
DA0
CS0
DASP
5V
5V
GND
GND
CSEL
KX14-50K5D
C1
470/10V
R3
NM
CSEL_HOST
R1
47K
R2
47K
5V
J1
2-3:SLAVE
1-2:MASTER
2211S-03G
Short plug on pin1-2:
CDRW900SL/CDRW901SL/CC222SL
P2
3675P40VUAO
P3
5V
GND
GND
12V_or_NC
B4B-EH
From Powr supply

PCB,SLIM

CD-RW901
Wiring Diagram

